

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application.

**Listing of Claims:**

1. (Currently Amended) A method of driving a plasma display panel, the plasma display panel including discharge cells, each discharge cell formed at an intersection of a scan electrode and a sustain electrode, and a data electrode, the method comprising:

dividing one field period into a plurality of sub-fields, each sub-field having an initializing period, writing period, and sustaining period; and

in the initializing periods of the plurality of sub-fields, performing one of all-cell initializing operation and selective initializing operation, wherein, the all-cell initializing operation causes initializing discharge in all the discharge cells for displaying an image, and the selective initializing operation selectively causes initializing discharge only in the discharge cells subjected to sustaining discharge in the preceding sub-field;

wherein, each of the initializing periods for performing the all-cell initializing operation has

a former half part, a latter half part, and an abnormal charge erasing part,

in the former half part, application of an ascending ramp waveform voltage to the scan electrodes causes a first initializing discharge using the scan electrodes as anodes and the sustain electrodes and data electrodes as cathodes,

in the latter half part, application of a descending ramp waveform voltage which is ranging from a voltage with the same polarity as the voltage applied during the former half part of initialization period of a voltage reverse in polarity thereto, to the scan electrodes causes a second initializing discharge using the scan electrodes as the cathodes and the sustain electrodes and data electrodes as the anodes, and

in an abnormal charge erasing part, which causes self-erasing discharge in the discharge cells having excessive wall charge accumulated therein, the abnormal charge erasing part applying a positive rectangular waveform voltage with same polarity as the voltage applied during the former half part, followed by applying a negative rectangular waveform voltage reverse in polarity as the voltage applied during the former half part, to the scan electrodes; and

wherein, in the initializing periods for performing the selective initializing operation, a descending ramp waveform voltage is applied to the scan electrodes, using the scan electrodes as cathodes and the sustain electrodes and data electrodes as anodes.

2. (Previously Presented) The method of driving a plasma display panel according to claim 1,

wherein, in the abnormal charge erasing part, a voltage is not applied to the sustain electrode when a rectangular waveform voltage is applied.

3. (Cancelled)

4. (Previously Presented) The method of driving a plasma display panel according to claim 1, wherein a number of times of all-cell initializing period in the one field period is controlled by determining either the all-cell initializing operation or the selective initializing operation according to an APL.

5. (Previously Presented) The method of driving a plasma display panel according to claim 2, wherein a number of times of all-cell initializing period in the one field period is controlled by determining either the all-cell initializing operation or the selective initializing operation according to an APL.

- 6.-8. (Cancelled)